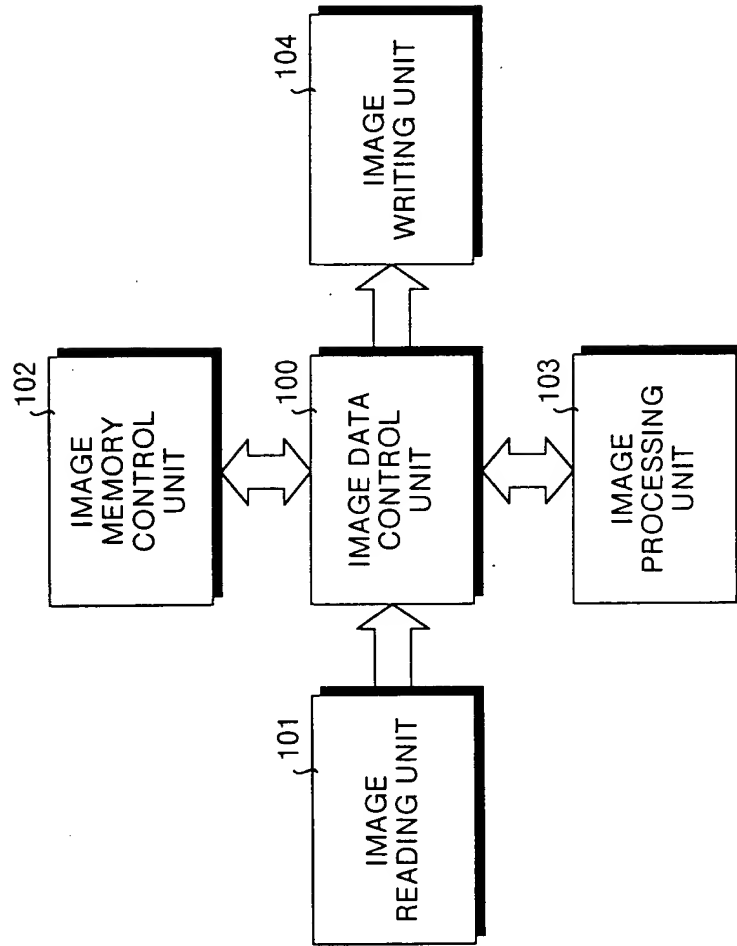


FIG.1



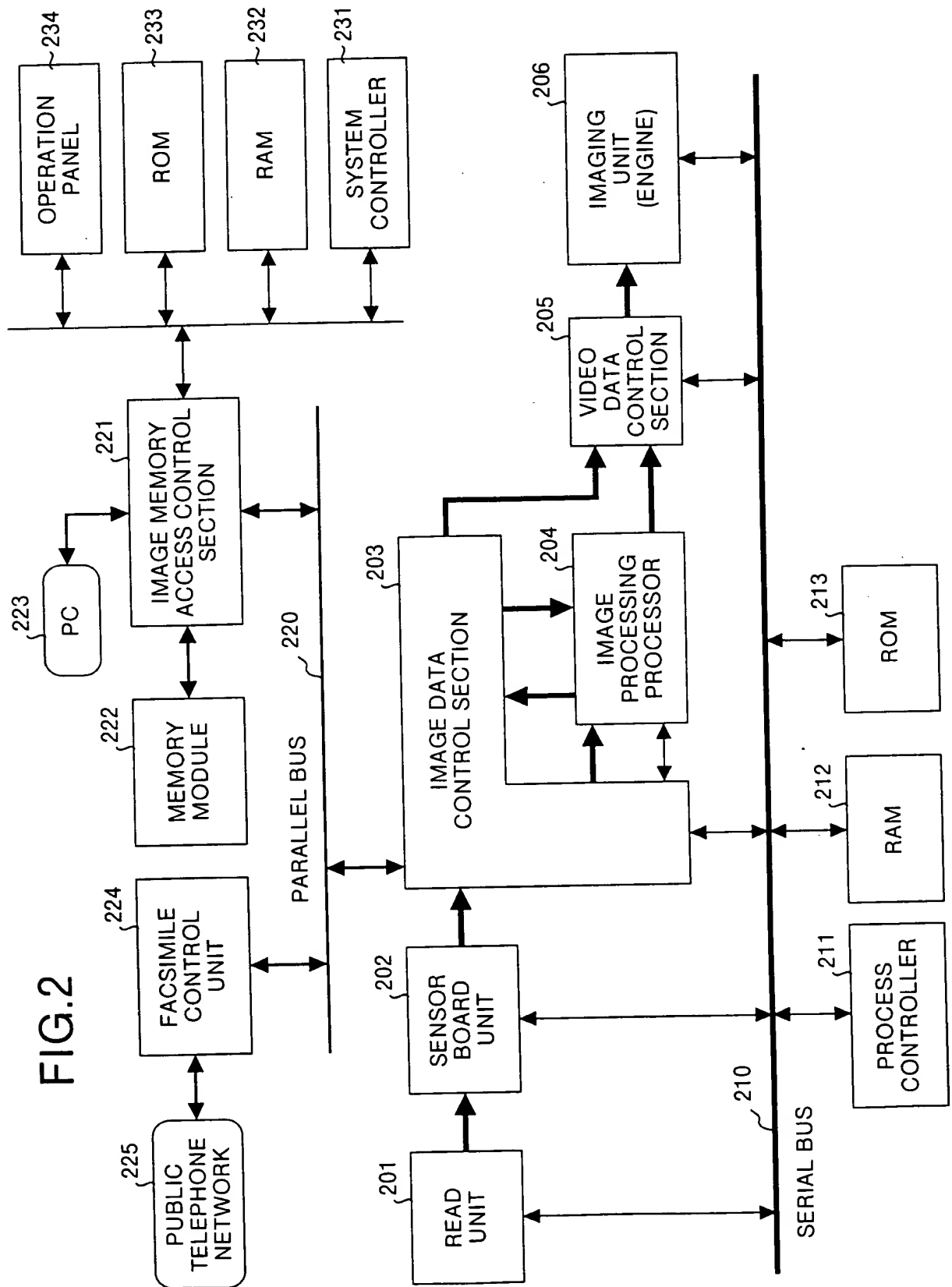
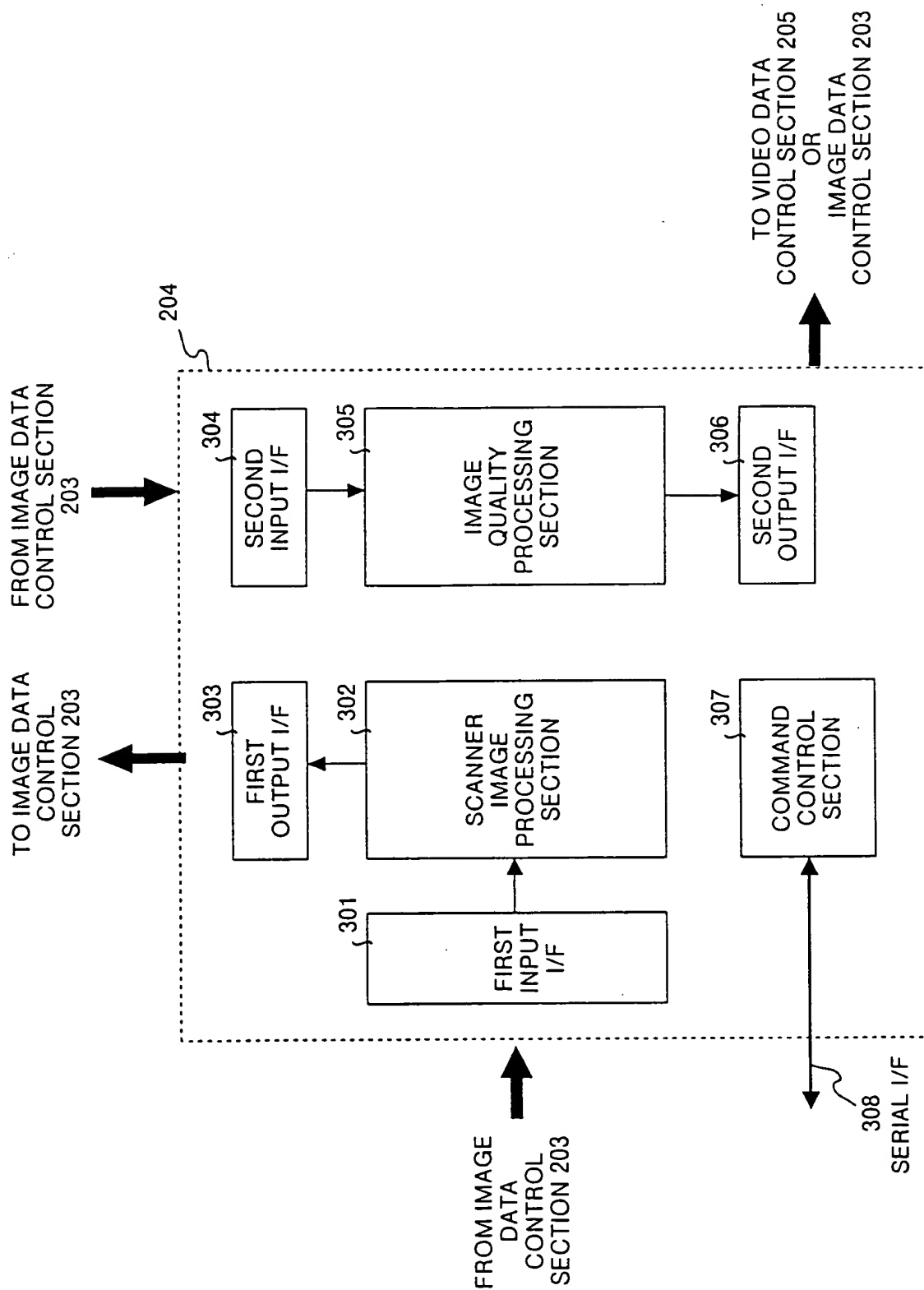


FIG.2

FIG. 2 is a block diagram of a facsimile system.

FIG.3



4/16

FIG. 4

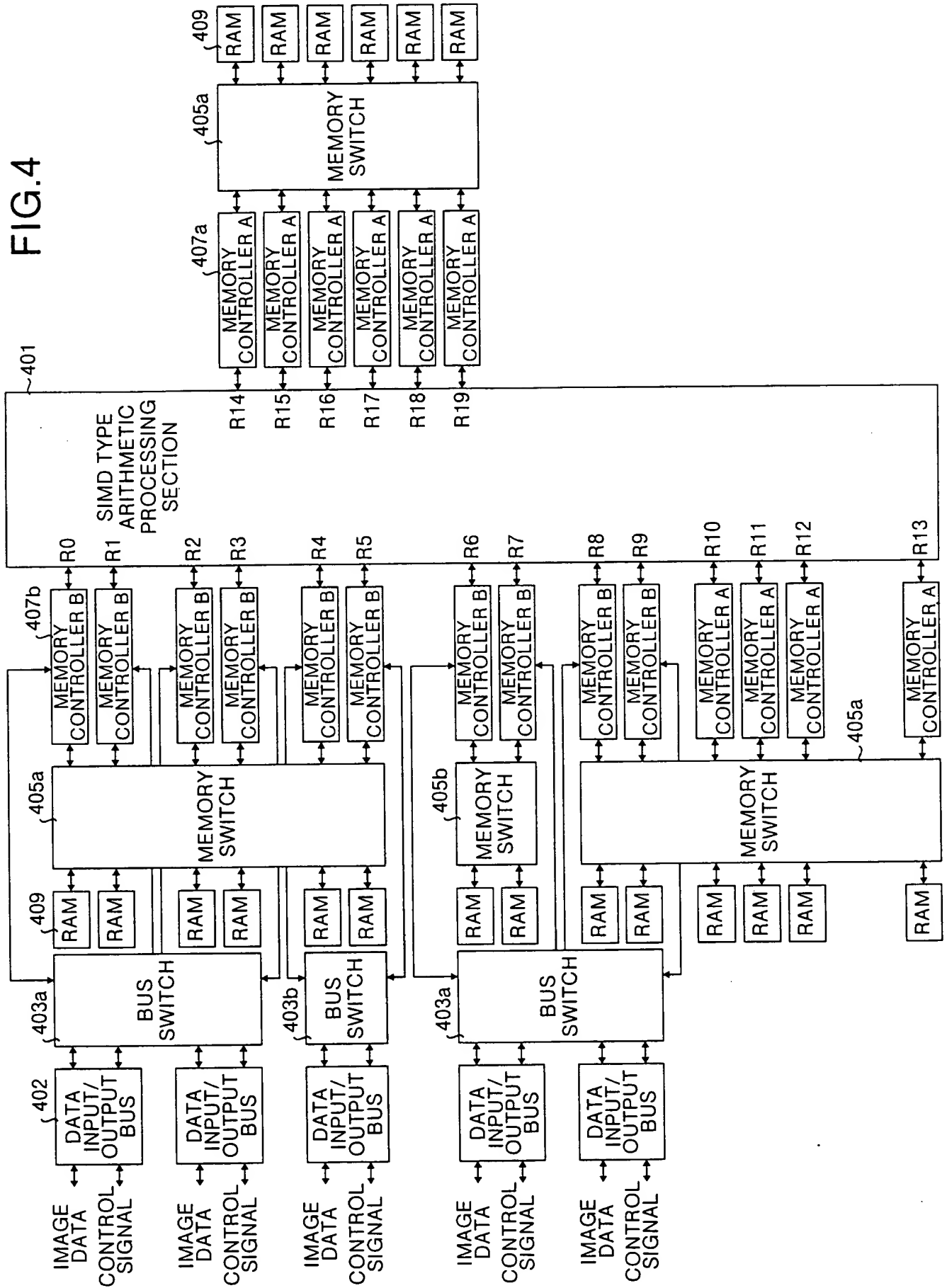
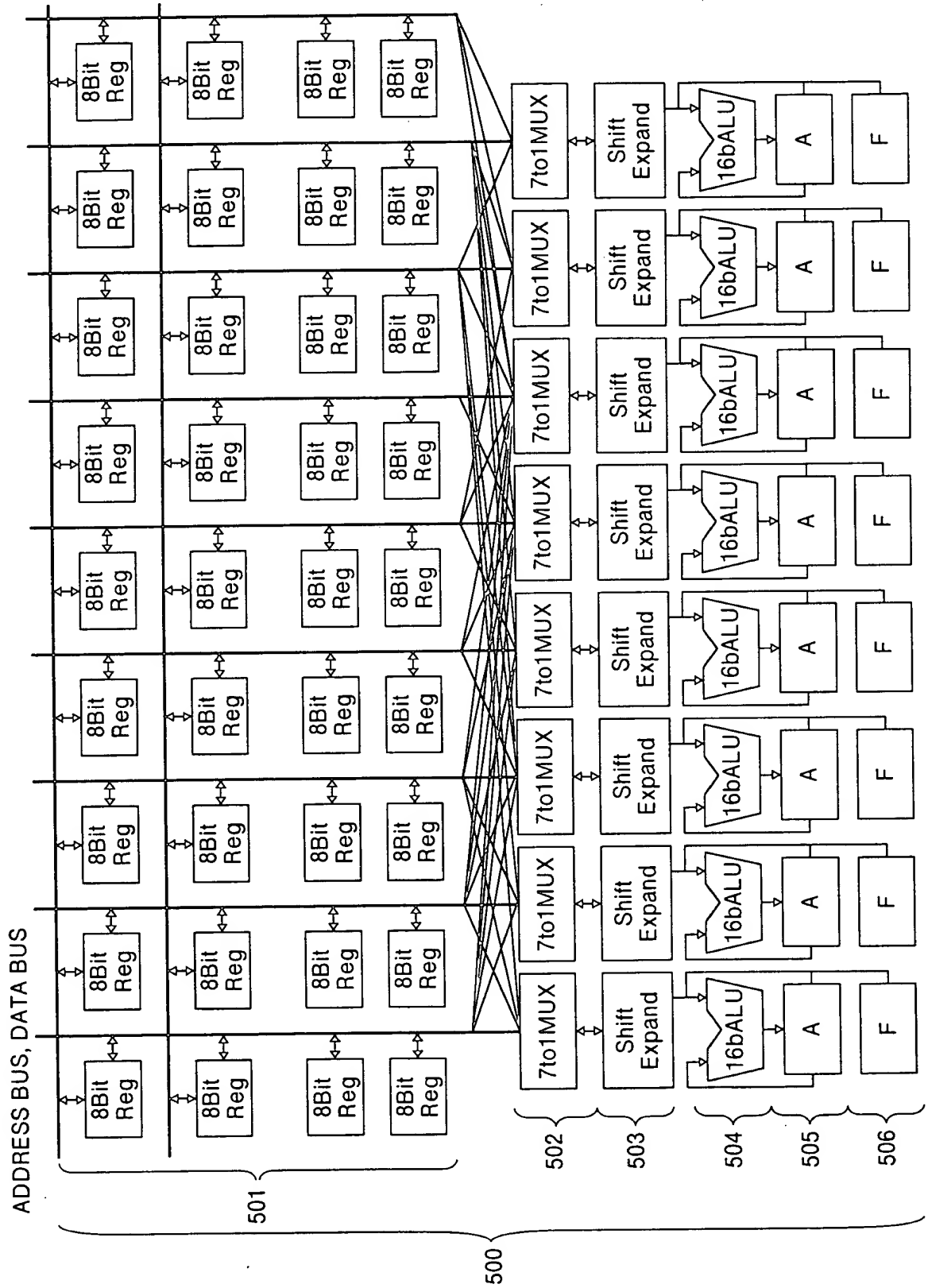


FIG. 5



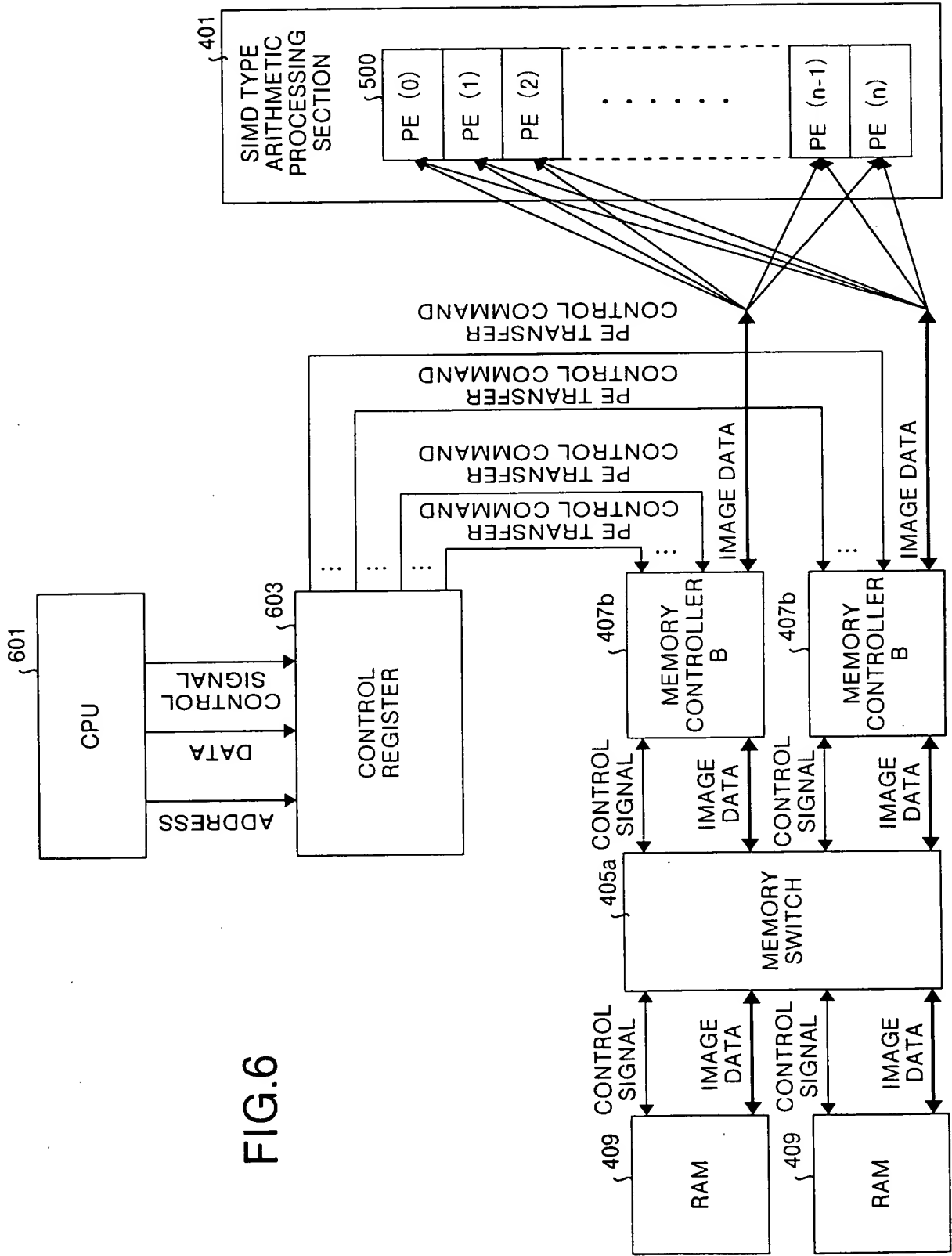


FIG.6

FIG. 6 is a block diagram of a SIMD type arithmetic processing section 401.

FIG.7

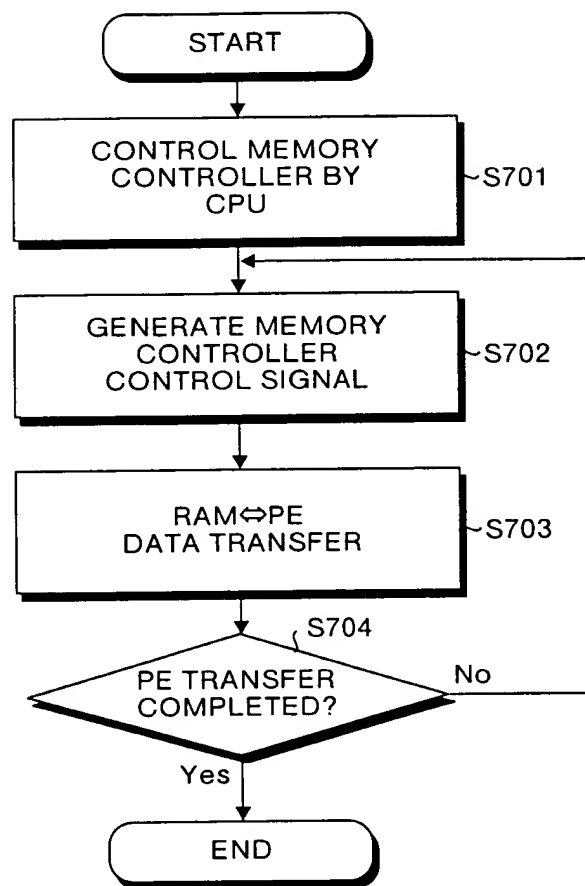
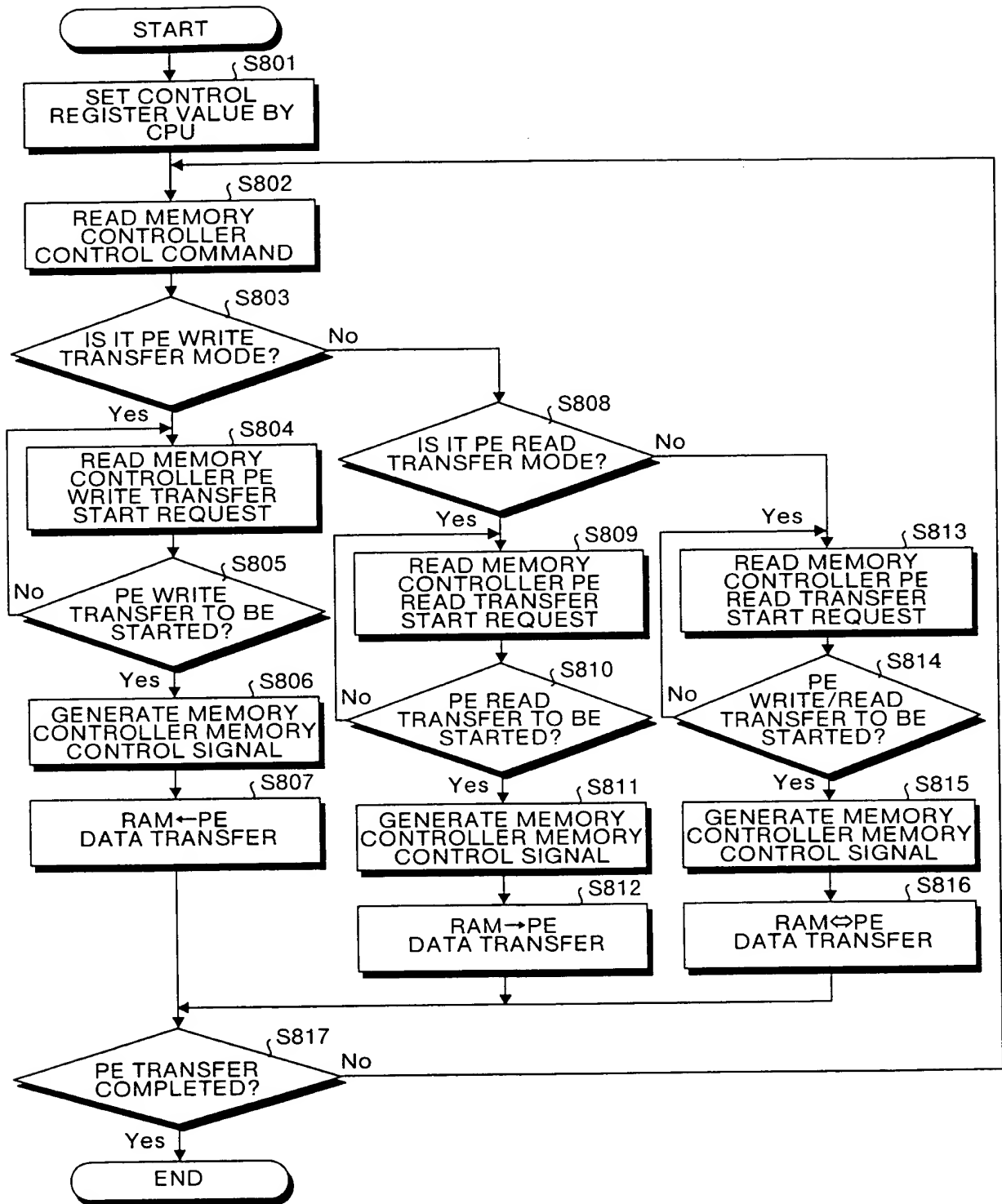


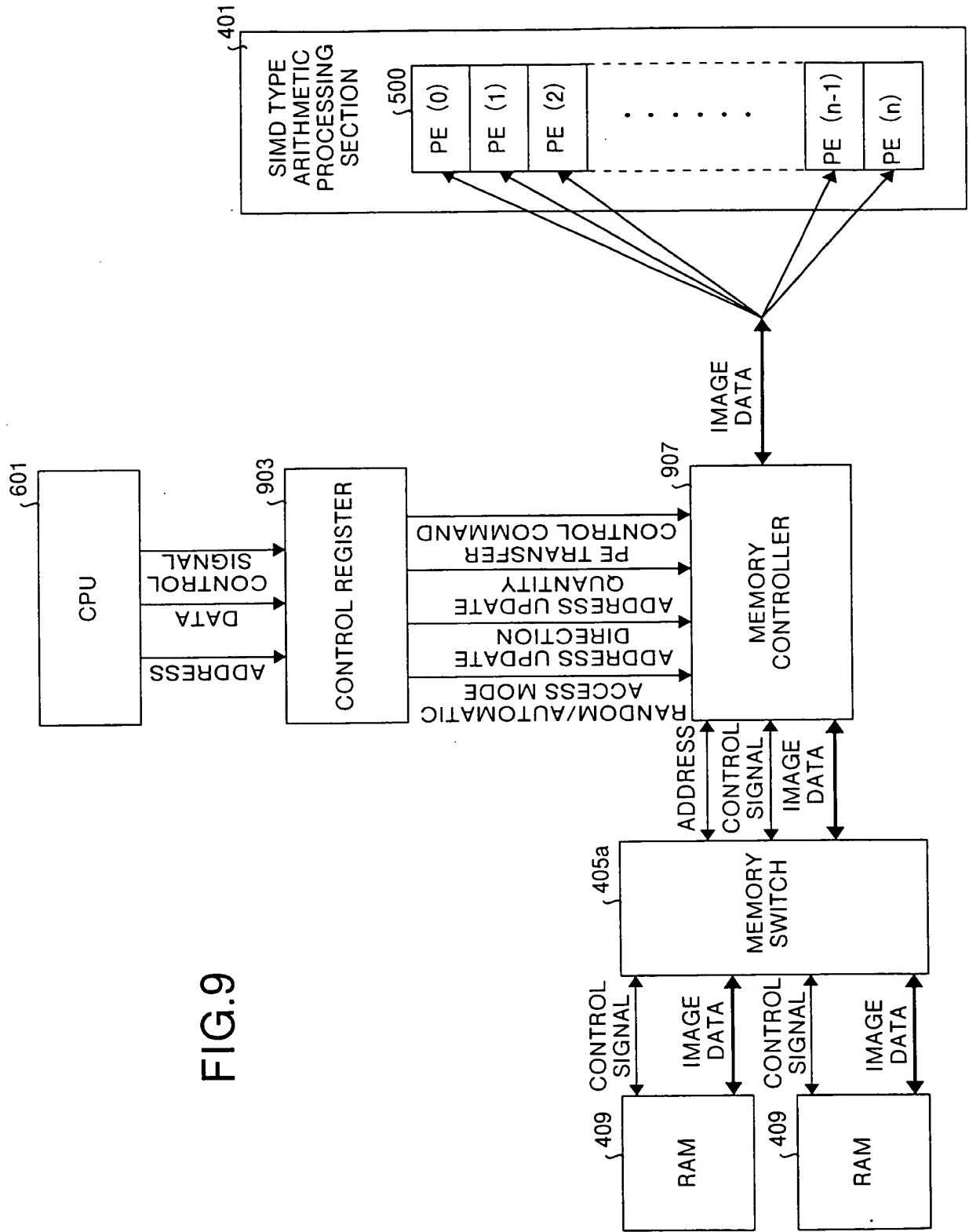
FIG.8





9/16

FIG.9



10/16

FIG.10

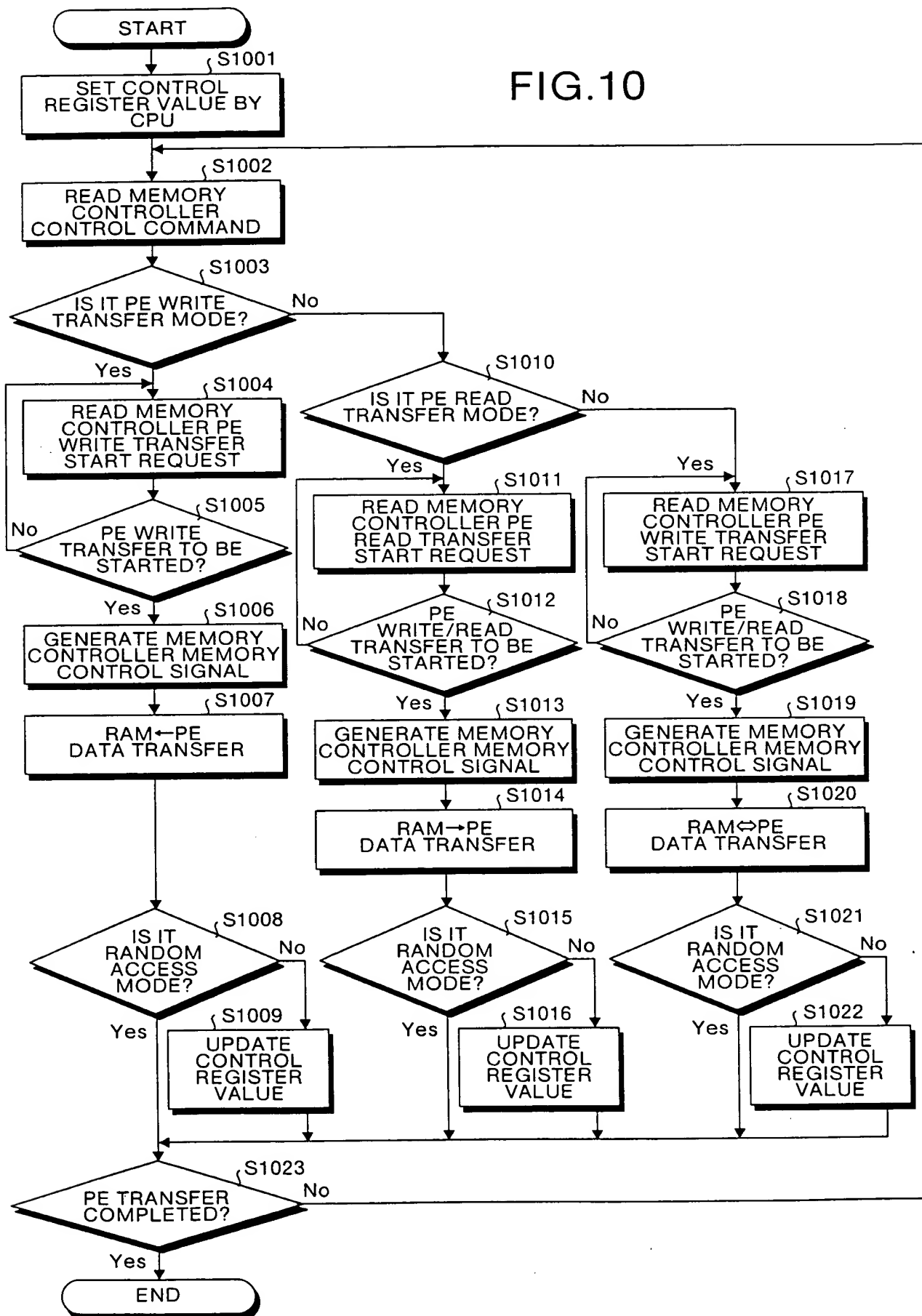


FIG.11

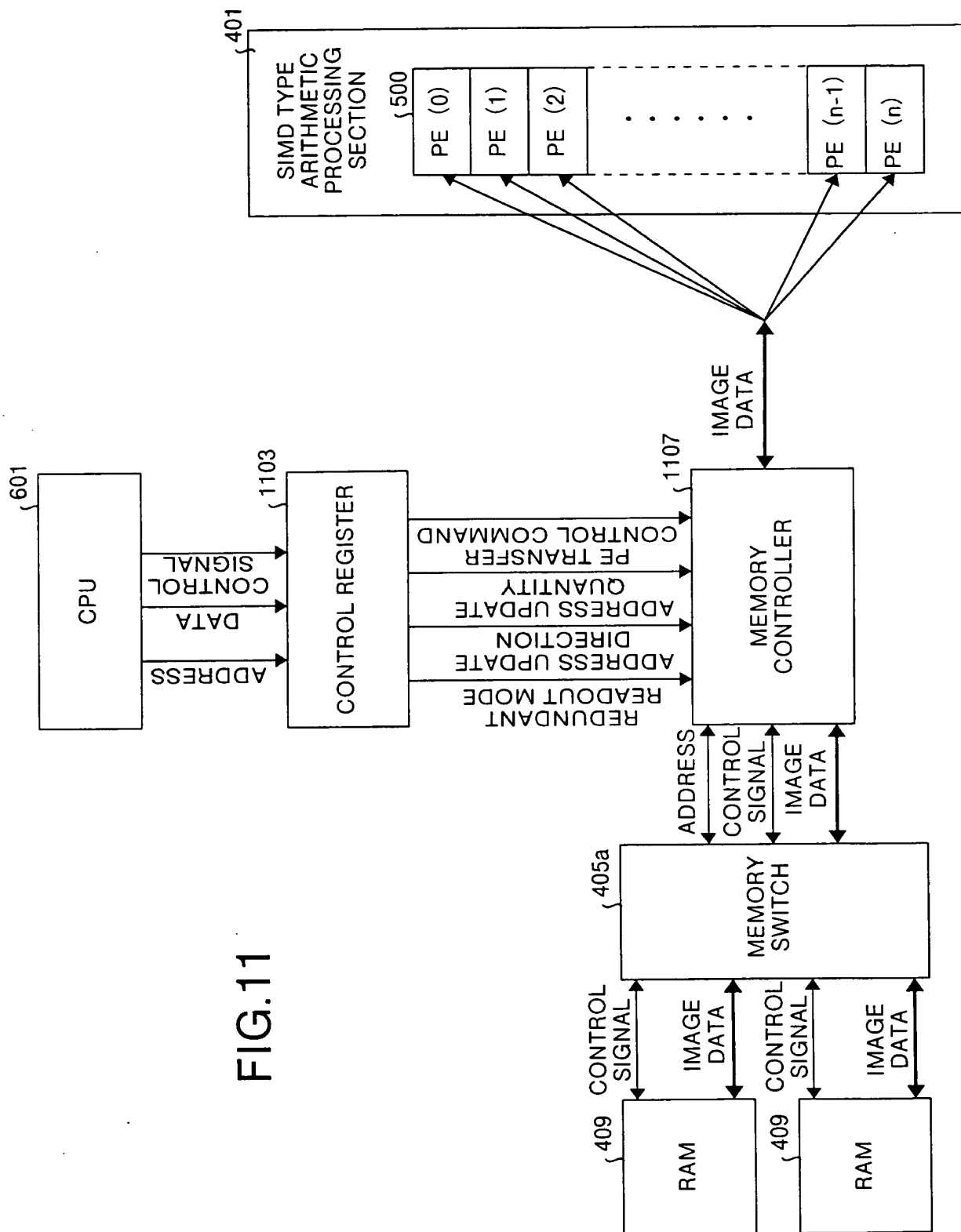
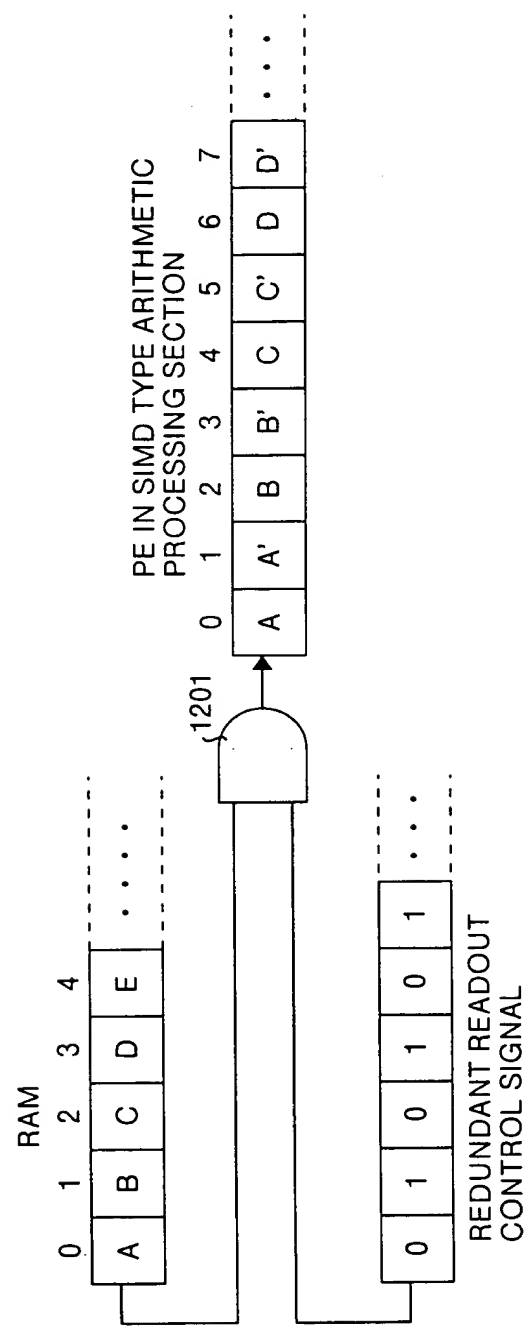
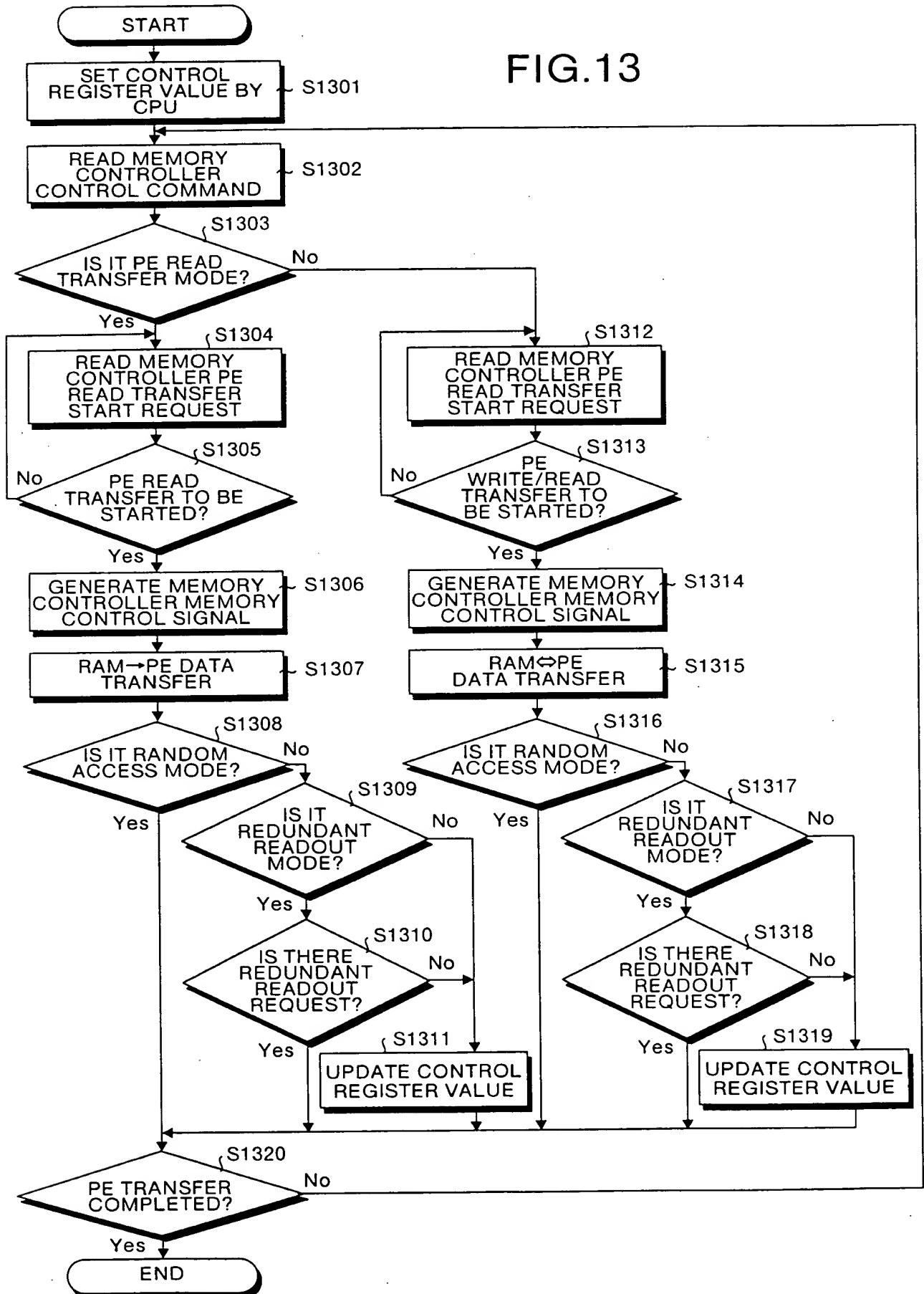


FIG.12



13/16

FIG.13



14/16

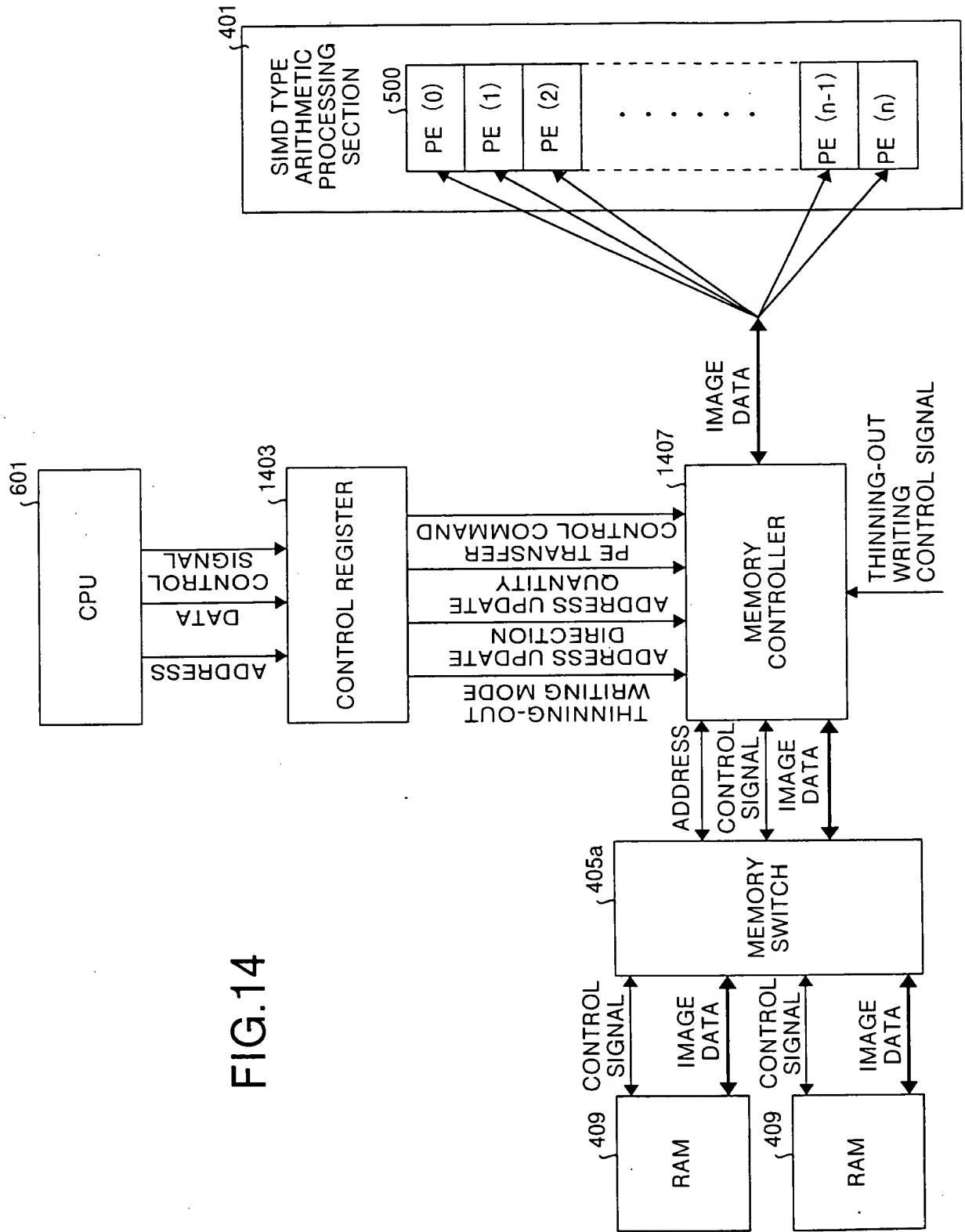


FIG.14

FIG.15

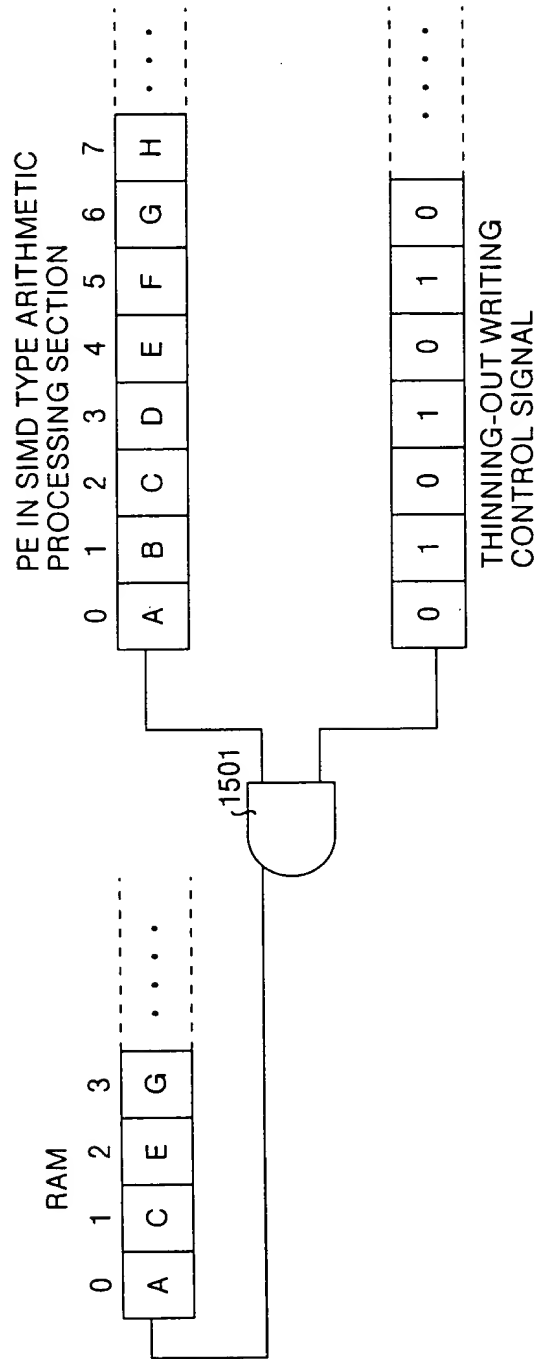


FIG.16

